IN THE CLAIMS:

1. (Currently Amended) An on-screen display apparatus comprising:

a voltage holding means for holding a voltage value ~~at~~ of an input chroma signal DC level ~~at~~ within a period ~~time~~ during which ~~when~~ the input chroma signal is ~~a~~ null ~~signal~~ and ~~is for~~ outputting the voltage value during an on-screen display period, reducing variations in the DC level when switching between the input chroma signal and an on-screen display signal.

2. (Currently Amended) An on-screen display apparatus comprising:

a voltage holding means for holding a voltage value at of an input chroma signal DC level ~~at~~ within a period ~~time~~ during which ~~when~~ the input chroma signal is ~~a~~ null ~~signal~~; and

a means for generating and outputting a chroma signal as a function of the voltage value during an on-screen display period, reducing variations in the DC level when switching between the input chroma signal and an on-screen display signal.

3. (Currently Amended) An on-screen display apparatus comprising:

a voltage holding means for holding a voltage value at of an input chroma signal DC level ~~at~~ within a period ~~time~~ during which ~~when~~ the input chroma signal is ~~a~~ null ~~signal~~; and

an output switch for outputting a voltage value held by the voltage holding means during an on-screen display period and outputting the input chroma signal at a period ~~time other than during~~ except for the on-screen display period, reducing variations in the DC level when switching between the input chroma signal and an on-screen display signal.

4. (Previously Presented) An on-screen display apparatus comprising:

a voltage holding means for holding a voltage value at a time when an input chroma signal is a null signal;

an AC component generation means for generating AC components of the chroma signal;

an adder for adding the voltage value held by the voltage holding means and the AC components of the chroma signal which are generated by the AC component generation means; and

an output switch for outputting the signal added by the adder during an on-screen display period and outputting the input chroma signal other than the on-screen display period.

5. (Previously Presented) The on-screen display apparatus of Claim 3, wherein the voltage holding means comprises a capacitor for holding a voltage value.

6. (Previously Presented) The on-screen display apparatus of Claim 5, wherein the voltage holding means further comprises a resistor located on a chroma signal input side of the capacitor.

7. (Currently Amended) The on-screen display apparatus of Claim 6, wherein

the voltage holding means is placed on a chroma signal input side of the resistor, and further comprises a hold timing switch that is brought into conduction when the input chroma signal is ~~a~~ null ~~signal~~.

8. (Currently Amended) The on-screen display apparatus of Claim 6, wherein

the voltage holding means is placed between the capacitor and the resistor, and further comprises a hold timing switch that is brought into conduction when the input chroma signal is ~~a~~ null ~~signal~~.

9. (Currently Amended) The on-screen display apparatus of Claim 3, wherein

the voltage holding means comprises:

an AD converter for converting an input chroma signal into a digital signal when the input chroma signal is ~~a~~ null ~~signal~~;

a storage means for storing a voltage value of the input chroma signal within ~~at~~ the period ~~time~~ during which ~~when~~ the input chroma signal is ~~a~~ null ~~signal~~, which has been converted into a digital signal by the AD converter; and

a DA converter for converting the voltage value stored in the storage means into an analog signal.

10. (Currently Amended) The on-screen display apparatus of Claim 3, wherein

the voltage holding means is for holding the voltage value during a horizontal sync period during ~~in~~ which the input chroma signal is ~~a~~ null ~~signal~~.

11. (Currently Amended) The on-screen display apparatus of Claim 3, wherein

the voltage holding means is for holding the voltage value during a vertical sync period during ~~in~~ which the input chroma signal is ~~a~~ null ~~signal~~.